

Abstract

An image capture and processing device includes an image sensor integrated circuit. A plurality of analogue-to-digital converters (ADC's) are connected to the image sensor integrated circuit to
5 convert analogue signals generated by the image sensor integrated circuit into digital signals.

Image processing circuitry is connected to the ADC's to carry out image processing operations on the digital signals. A print head interface is connected to the image processing circuitry to receive data from the image processing circuitry and to format that data correctly for a printhead.

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